



Peter Hopkins joined the Superconductive Electronics Group of the National Institute of Standards and Technology in Boulder, CO, in 2014. He has been head of the Flux Quantum Electronics Project since 2015, researching the design, fabrication and testing of superconductor electronics for energy-efficient computing, radio frequency waveform synthesis, and scalable cryogenic quantum computing. His team has also provided test and evaluation support for IARPA's Cryogenic Computing Complexity (C3) program to develop conventional superconducting computing and IARPA's SuperTools program, which is developing electronic design automation software tools for superconducting electronics. Prior to working at NIST, Dr. Hopkins spent 19 years in the data storage

industry where he productized hard-disk drives and solid-state drives for Quantum, Seagate, and Micron.